

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Christopher Wargo, et al. : Group
Serial No.: 10/612,904 : Art Unit # 1762
Filed: 7 July 2003 : Examiner:
Title: METHOD OF FORMING HIGH : Unknown
RESOLUTION ELECTRONIC
CIRCUITS ON A SUBSTRATE

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The Applicants wish to make the following art references of record in the above-identified Patent Application pursuant to 37 C.F.R. §§ 1.97 and 1.98, and to the Duty of Disclosure set forth in 37 C.F.R. § 1.56.

Although the information submitted herewith may be "material" to the Examiner's consideration of the subject Patent Application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search was made or that no other material information, as defined in 37 C.F.R. § 1.56(b), exists.

MR2799-10
Serial Number: 10/612,904

The cited Publications are:

<u>Ref. No.</u>	<u>Description</u>
AA	Vanheusden, et al., "Advanced Materials System for Ultra-Low Temperature, Digital, Direct-Write Technologies", DIRECT-WRITE TECHNOLOGIES FOR RAPID PROTOTYPING APPLICATIONS, Academic Press, San Diego, 2002, pgs 123 - 174
AB	"Electronic Packaging and Interconnect Handbook", 1991, pgs 7.1 - 7.19.

This Information Disclosure Statement is being filed more than three months subsequent to the Filing Date of the subject Patent Application, but before the mailing of the first Office Action.

MR2799-10
Serial Number: 10/612,904

A Form PTO-1449 and copies of the references are submitted along with this document. It is requested that the Examiner consider the references and make them of record in the above-referenced Patent Application.

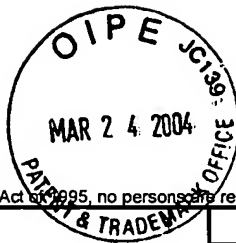
Respectfully submitted,
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Approved for use through 04/30/2003. OMB 0651-0031

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/612,904
Filing Date	7 JULY 2003
First Named Inventor	CHRISTOPHER WARGO, ET AL.
Art Unit	1762
Examiner Name	
Attorney Docket Number	MR2799-10

Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	Vanheusden, et al., "Advanced Materials System for Ultra-Low Temperature, Digital, Direct-Write Technologies", DIRECT-WRITE TECHNOLOGIES FOR RAPID PROTOTYPING APPLICATIONS, Academic Press, San Diego, 2002, pgs 123 - 174	
	AB	"Electronic Packaging and Interconnect Handbook", 1991, pgs 7.1 - 7.19.	

Examiner
SignatureDate
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

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